

REMARKS

In the present Office Action, claims 1-4, 6, and 8-20 were pending before the Office. Of these, claims 1 and 10 were the only independent claims.

Claims 1-4, 6, and 8-20 were rejected under 35 U.S.C. § 102.

No claims are hereby added, amended, canceled, or withdrawn.

A. CLAIM REJECTION UNDER 35 U.S.C. § 102

Claims 1-4, 6, and 8-20 stand rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 7,055,069 to Isom, III et al. [hereinafter *Isom*]. For at least the reasons set forth herein, Applicants respectfully traverse this rejection.

Independent claim 1 recites, inter alia:
employing one of a plurality of input lines
coupled to a processor to receive a test
signal for the processor wherein the
processor is positioned internally within the
IC.

Independent claim 10 recites, inter alia:
a processor within the IC;
a plurality of input lines coupled to the
processor positioned internally within the
IC;

...
wherein the apparatus is adapted to:
employ one of the plurality of the input
lines to receive a test signal for the
processor[.]

Applicants respectfully submit that the cited portion of *Isom* fails to disclose the above features. Accordingly, Applicants respectfully submit that the Office Action fails to establish a prima facie case of anticipation.

Initially, Applicants note that the cited portion of *Isom* does not appear to make any mention of a "test signal." For this reason alone, Applicants respectfully submit that the cited portion of *Isom* cannot be properly relied upon for disclosing the above features.

Isom relates to integrated circuits. *Isom*, col. 1, lines 5-7. *Isom* discloses an integrated circuit package 100 containing a spare input/output buffer circuit 302. *Isom*, FIG. 3.

In contrast to the above features of the present application, the cited portion of *Isom* merely shows a spare input/output buffer circuit 302. The cited portion of *Isom* fails to disclose a plurality of input lines coupled to a processor. Further, the cited portion of *Isom* fails to show such processor being positioned internally within the integrated circuit. Accordingly, Applicants respectfully submit that *Isom* cannot properly be relied upon for disclosing the above features.

Applicants respectfully submit that for at least the above reasons, the Office Action fails to establish a prima facie case of anticipation. Accordingly, withdrawal of the §102 rejection is respectfully requested.

C. CONCLUSION


Since Applicants asserts that all the independent claims are in condition for allowance and all remaining claims properly depend from the independent claims, Applicants assert that all claims are allowable.

Applicants do not believe a request for extension of time is required but if it is, please accept this paragraph as a request for an extension of time and authorization to charge the requisite extension fee to Deposit Account No. 04-1696. Applicant does not believe any additional fees are due regarding

this amendment. However, if any additional fees are required,
please charge Deposit Account No. 04-1696.

Respectfully Submitted,

Dated: October 24, 2008
Hawthorne, New York



Steven M. Santisi
Registration No. 40,157
Dugan & Dugan, PC
Attorneys for Applicants
(914) 579-2200